

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S29	24595	(liquid near3crystal 'lcd' 'tft' transistor) with (glass near3 substrate)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:45
S30	5356	(liquid near3crystal 'lcd' 'tft' transistor) with (common near3 substrate)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/03/30 13:51
S31	360	(liquid near3crystal 'lcd' 'tft' transistor) with ((common near3 substrate) near4 glass)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:46
S32	2637	S30 and pixel	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/03/30 13:51
S33	2110	S32 and gate	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/03/30 14:00
S34	1	10/712939	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/03/30 14:33
S35	1	10/452000	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/03/30 14:33
S36	1	10/712939	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 12:15
S37	25438	(liquid near3crystal 'lcd' 'tft' transistor) with (glass near3 substrate)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:01
S38	166864	(liquid near3 crystal 'lcd' 'tft' transistor switch\$3) with substrate	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:47

S39	4497	(liquid near3 crystal 'lcd' 'tft' transistor switch\$3) with array adj substrate	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:48
S40	1291	S39 and (liquid near3 crystal 'lcd' 'tft' transistor switch\$3) with size\$2	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:49
S41	378	S40 and (liquid near3 crystal 'lcd' 'tft' transistor switch\$3) with mask\$4	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:50
S42	359	S41 and gate	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:50
S43	338	S42 and source	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:05
S44	325	S43 and drain	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:05
S45	324	S44 and electrode	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:05
S46	324	S45 and array	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:06
S47	153	S46 and (transistor switch\$3) near4 array	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:55
S48	138	S47 and pixel	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 13:56
S49	25997	(liquid near3crystal 'lcd' 'tft' transistor switch\$3) with (glass near3 substrate)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:04

S50	1904	S49 and ((differen\$3 various) with (glass near3 substrate))	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:33
S51	702	S50 and (liquid near3crystal 'led' 'tft' transistor switch\$3) with (gate)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:05
S52	623	S51 and source	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:05
S53	562	S52 and drain	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:05
S55	556	S53 and electrode	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:06
S57	291	S55 and array	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:07
S58	28	S57 and ((differen\$3 various) near5 (driving panel module)) with (glass near3 substrate)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/14 15:34